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Docket No.: 60188-124

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yasuyuki DOI, et al.

Serial No.: 10/019437

Group Art Unit:

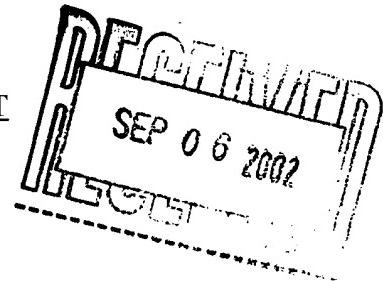
Filed: December 31, 2001

Examiner:

For: LIQUID CRYSTAL DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, REFERENCE VOLTAGE BUFFERING CIRCUIT, AND METHOD FOR CONTROLLING THE SAME

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents and Trademarks
Washington, D. C. 20231



Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above identified application and readily available to the Examiner.

Serial No.:

Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. Acknowledgement and consideration of these documents are respectfully requested.

Respectfully submitted,

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